

CLAIMS

What is claimed is:

1. A remote server management controller disposed in a managed server, the remote server management controller comprising:
an IOP;
an embedded JTAG master that is controllable by the IOP, the embedded JTAG master having a JTAG interface; and
at least one integrated circuit disposed in the managed server and connected for operative communication to the JTAG interface, and
wherein the JTAG master is adapted to be accessed remotely through the remote server management controller to provide communication between a user and the at least one integrated circuit via the JTAG interface.
2. The remote server management controller of claim 1 wherein the JTAG interface comprises an ITP interface.
3. The remote server management controller of claim 1 wherein the JTAG interface comprises an ICE interface.
4. The remote server management controller of claim 1 wherein the at least one integrated circuit comprises a microprocessor.

5. The remote server management controller of claim 1 wherein the at least one integrated circuit comprises a component of a chipset.
6. The remote server management controller of claim 1 wherein the JTAG master is adapted to program the at least one integrated circuit.
7. The remote server management controller of claim 1 wherein the IOP is adapted to be programmed to control the JTAG master to perform an initial test of the at least one integrated circuit when the managed server is powered up.
8. The remote server management controller of claim 1 wherein the IOP is programmed with descriptive data about the at least one integrated circuit.
9. A managed server, comprising:
 - a motherboard having at least one integrated circuit disposed thereon;
 - a remote server management controller in operable communication with the managed server, the remote server management controller comprising:
 - an IOP;
 - an embedded JTAG master that is controllable by the IOP, the embedded JTAG master having a JTAG interface, the at least one integrated circuit connected for operative communication to the JTAG interface;

wherein the JTAG master is adapted to be accessed remotely through the remote server management controller to provide communication between a user and the at least one integrated circuit via the JTAG interface.

- 5 10. The managed server of claim 9 wherein the JTAG interface comprises an ITP interface.
11. The managed server of claim 9 wherein the at least one integrated circuit comprises a microprocessor.
- 10 12. The managed server of claim 9 wherein the JTAG master is adapted to program the at least one integrated circuit.
13. The managed server of claim 9 wherein the at least one integrated circuit comprises a component of a chipset.
- 15 14. The managed server of claim 9 wherein the IOP is adapted to be programmed to control the JTAG master to perform an initial test of the at least one integrated circuit when the managed server is powered up.
- 20 15. The managed server of claim 9 wherein the IOP is programmed with descriptive data about the at least one integrated circuit.

16. A method of communicating with an integrated circuit in a managed server, the managed server having a remote server management controller in operative communication therewith, the remote server management controller having an IOP and a JTAG master disposed thereon for operative communication with each other, the JTAG master having a JTAG interface connected for operative communication to the integrated circuit, the method comprising the acts of:
- receiving data at the IOP of the remote server management controller;
- transmitting the data from the IOP to the JTAG master;
- transmitting the data from the JTAG master to the integrated circuit via the JTAG interface.
17. The method of claim 16, further comprising the act of programming the IOP to control the JTAG master to perform a boundary scan of the integrated circuit when the managed server is powered up.
18. The method of claim 16 further comprising the act of programming the IOP with descriptive data about the integrated circuit.
19. The method of claim 16 further comprising the act of programming the integrated circuit.
20. The method of claim 16 wherein the recited acts are preformed in the recited order.